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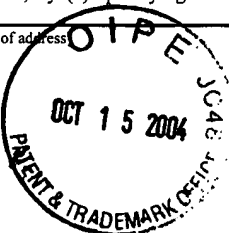
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Stephen A. Terrile	(Depositor's name)
<i>[Signature]</i>	(Signature)
October 11, 2004	(Date)

10/18/2004 FMETEK12 00000050 10682566

01 FC:1501 1370.00 OP
02 FC:1504 300.00 OP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/682,566	10/09/2003	John L. Melanson	1463-CA	8472

TITLE OF INVENTION: OVERLOAD PROTECTION AND STABILITY FOR HIGH ORDER 1-BIT DELTA-SIGMA MODULATORS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	10/12/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, LINH V	2819	341-143000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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1 Hamilton & Terrile, LLP
2 Kent B. Chambers
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Cirrus Logic, Inc.

Austin, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is not claiming SMALL ENTITY status. See, e.g., 37 CFR 1.27(g)(2).

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